

Claims

- [c1] What is claimed is:
1. A central processing unit (CPU) interface card having an accelerated graphic port (AGP) for adapting to a computer backplane to form a computer system, the CPU interface card comprising:
- a plurality of extended industry standard architecture (EISA) contact pads corresponding to an EISA bus for electrically connecting to the computer backplane, the EISA contact pads transferring data between the computer backplane and CPU card according to the EISA bus; and
- a plurality of AGP contact pads interlaced with the EISA contact pads, in compliance with the standard configuration of the EISA contact pads to constitute an AGP bus thereby transferring image data between the CPU interface card and the computer backplane.
- [c2] 2. The CPU interface card as described in claim 1 wherein the AGP contact pads of the CPU interface card are inserted into and then electrically connected with an EISA expansion slot of the computer backplane for image data transmission.
- [c3] 3. The CPU interface card as described in claim 2 wherein the EISA contact pads of the CPU interface card are inserted into and then electrically connected with the EISA expansion slot of the computer backplane for data transmission.
- [c4] 4. The CPU interface card as described in claim 1 wherein the AGP contact pads of the CPU interface card are interlaced with the EISA contact pads to increase disposition variation of the AGP contact pads and the EISA contact pads on the CPU interface for space saving.
- [c5] 5. A CPU interface card having an AGP for adapting to a computer backplane to form a computer system, the CPU interface card comprising at least:
- a plurality of peripheral component interconnect (PCI) contact pads corresponding to a PCI bus which serves data transmissions between the computer backplane and the CPU interface card when the PCI contact pads are electrically connected to the computer backplane; and
- a plurality of AGP contact pads disposed apart from the PCI contact pads in a predetermined distance to constitute an AGP bus thereby transferring image

data between the computer backplane and the CPU interface card when the AGP contact pad are electrically connected to the computer backplane.

- [c6] 6.The CPU interface card as described in claim 5 wherein the AGP contact pads of the CPU interface card are inserted into and then electrically connected to an AGP expansion slot of the computer backplane for image data transmission.
- [c7] 7.The CPU interface card as described in claim 6 wherein the PCI contact pads of the CPU interface card are inserted into and then electrically connected to a PCI expansion slot of the computer backplane for data transmission wherein the PCI expansion slot is disposed in alignment with the AGP expansion slot.
- [c8] 8.A CPU interface card having an AGP for adapting to a computer backplane to form a computer system, the CPU interface card comprising:
a plurality of PCI contact pads corresponding to a PCI bus which serves data transmission between the computer backplane and the CPU interface card when the PCI contact pads electrically connected to the computer backplane;
a plurality of EISA contact pads disposed apart from the PCI contact pads in a predetermined distance to constitute a EISA bus which serves data transmission between the computer backplane and the CPU interface card when the EISA contact pads are electrically connected to the computer backplane; and
a plurality of AGP contact pads interlaced with the EISA contact pads, in compliance with the standard configuration of the EISA contact pads to constitute an AGP bus which serves data transmission between the computer backplane and the CPU interface card when the AGP contact pads are electrically connected to the computer backplane
- 9.The CPU interface card as described in claim 8 wherein the AGP contact pads of the CPU interface card are inserted into and then electrically connected to an EISA expansion slot disposed on the computer backplane for image data transmission.
- [c9] 10.The CPU interface card of claim 9 wherein the EISA contact pads of the CPU interface card are inserted into and then electrically connected to the EISA expansion slot of the computer backplane for data transmission.

- [c10] 11.The CPU interface card of claim 10 wherein the AGP contact pads are interlaced with the EISA contact pads to increase disposition variation of the AGP contact pads and the EISA contact pads for space saving.
- [c11] 12.The CPU interface card of claim 8 wherein the PCI contact pads of the CPU interface card are inserted into and then electrically connected to a PCI expansion slot for data transmission.
- [c12] 13.The CPU interface card of claim 8 wherein the PCI contact pads and the EISA contact pads are disposed in alignment on the CPU interface card for proper respective insertion into the PCI expansion slot and the EISA expansion slot.